

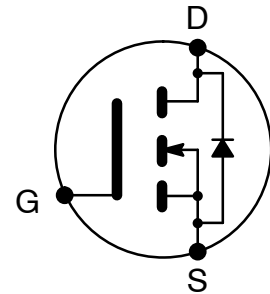


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NTE2940 MOSFET N-Channel, Enhancement Mode High Speed Switch TO220 Full Pack Type Package

Features:

- Low Static Drain-Source ON Resistance
- Improved Inductive Ruggedness
- Fast Switching Times
- Low Input Capacitance
- Extended Safe Operating Area
- TO220 Type Isolated Package



Absolute Maximum Ratings:

| | |
|---|-------------------------------|
| Drain-Source Voltage (Note 1), V_{DSS} | 60V |
| Drain-Gate Voltage ($R_{GS} = 1M\pm$, Note 1), V_{DGR} | 60V |
| Gate-Source Voltage, V_{GS} | $\pm 20V$ |
| Drain Current, I_D | |
| Continuous | |
| $T_C = +25^\circ C$ | 15A |
| $T_C = +100^\circ C$ | 10A |
| Pulsed (Note 2) | 60A |
| Gate Current (Pulsed), I_{GM} | $\pm 1.5A$ |
| Single Pulsed Avalanche Energy (Note 3), E_{AS} | 9.5mJ |
| Avalanche Current, I_{AS} | 15A |
| Total Power Dissipation ($T_C = +25^\circ C$), P_D | 48W |
| Derate Above $25^\circ C$ | 0.32W/ $^\circ C$ |
| Operating Junction Temperature Range, T_J | -55° to $+175^\circ C$ |
| Storage Temperature Range, T_{stg} | -55° to $+175^\circ C$ |
| Maximum Lead Temperature (During Soldering, 1/8" from case, 5sec), T_L | $+300^\circ C$ |
| Thermal Resistance: | |
| Maximum Junction-to-Case, R_{thJC} | 3.12K/W |
| Typical Case-to-Sink (Mounting surface flat, smooth, and greased), R_{thCS} | 0.5K/W |
| Maximum Junction-to-Ambient (Free Air Operation), R_{thJA} | 62.5K/W |

Note 1. $T_J = +25^\circ$ to $+175^\circ C$.

Note 2. Repetitive Rating: Pulse width limited by maximum junction temperature.

Note 3. $L = 100 \geq H$, $V_{DD} = 25V$, $R_G = 25\pm$, Starting $T_J = +25^\circ C$.

Electrical Characteristics: ($T_C = +25^\circ\text{C}$ unless otherwise specified)

| Parameter | Symbol | Test Conditions | Min | Typ | Max | Unit |
|---|--------------|---|-----|------|------|---------------|
| Drain–Source Breakdown Voltage | BV_{DSS} | $V_{GS} = 0V, I_D = 250\mu\text{A}$ | 60 | – | – | V |
| Gate Threshold Voltage | $V_{GS(th)}$ | $V_{DS} = V_{GS}, I_D = 250\mu\text{A}$ | 2.0 | – | 4.0 | V |
| Gate–Source Leakage Forward | I_{GSS} | $V_{GS} = 20V$ | – | – | 100 | nA |
| Gate–Source Leakage Reverse | I_{GSS} | $V_{GS} = -20V$ | – | – | -100 | nA |
| Zero Gate Voltage Drain Current | I_{DSS} | $V_{DS} = \text{Max. Rating}, V_{GS} = 0$ | – | – | 250 | μA |
| | | $V_{DS} = 0.8 \text{ Max. Rating}, T_C = +150^\circ\text{C}$ | – | – | 1000 | μA |
| Static Drain–Source ON Resistance | $R_{DS(on)}$ | $V_{GS} = 10V, I_D = 8A, \text{Note 4}$ | – | – | 0.10 | \pm |
| Forward Transconductance | g_{fs} | $V_{DS} \geq 50V, I_D = 8A, \text{Note 4}$ | 5.6 | – | – | mhos |
| Input Capacitance | C_{iss} | $V_{GS} = 0V, V_{DS} = 25V, f = 1\text{MHz}$ | – | 635 | – | pF |
| Output Capacitance | C_{oss} | | – | 218 | – | pF |
| Reverse Transfer Capacitance | C_{rss} | | – | 105 | – | pF |
| Turn–On Delay Time | $t_{d(on)}$ | $V_{DD} = 0.5 BV_{DSS}, I_D = 15A, Z_O = 24\pm, \text{(MOSFET switching times are essentially independent of operating temperature)}$ | – | – | 30 | ns |
| Rise Time | t_r | | – | – | 90 | ns |
| Turn–Off Delay Time | $t_{d(off)}$ | | – | – | 40 | ns |
| Fall Time | t_f | | – | – | 30 | ns |
| Total Gate Charge (Gate–Source Plus Gate–Drain) | Q_g | $V_{GS} = 10V, I_D = 15A, V_{DS} = 0.8 \text{ Max. Rating}, \text{(Gate charge is essentially independent of operating temperature)}$ | – | – | 33 | nC |
| Gate–Source Charge | Q_{gs} | | – | 6.3 | – | nC |
| Gate–Drain (“Miller”) Charge | Q_{gd} | | – | 12.3 | – | nC |
| Source–Drain Diode Ratings and Characteristics | | | | | | |
| Continuous Source Current | I_S | (Body Diode) | – | – | 1.5 | A |
| Pulse Source Current | I_{SM} | (Body Diode) Note 2 | – | – | 60 | A |
| Diode Forward Voltage | V_{SD} | $T_J = +25^\circ\text{C}, I_S = 15A, V_{GS} = 0V, \text{Note 4}$ | – | – | 1.5 | V |
| Reverse Recovery Time | t_{rr} | $T_J = +25^\circ\text{C}, I_F = 15A, dI_F/dt = 100A/\mu\text{s}$ | – | – | 310 | ns |

Note 2. Repetitive Rating: Pulse width limited by maximum junction temperature.

Note 4. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.

